

Fig. 1

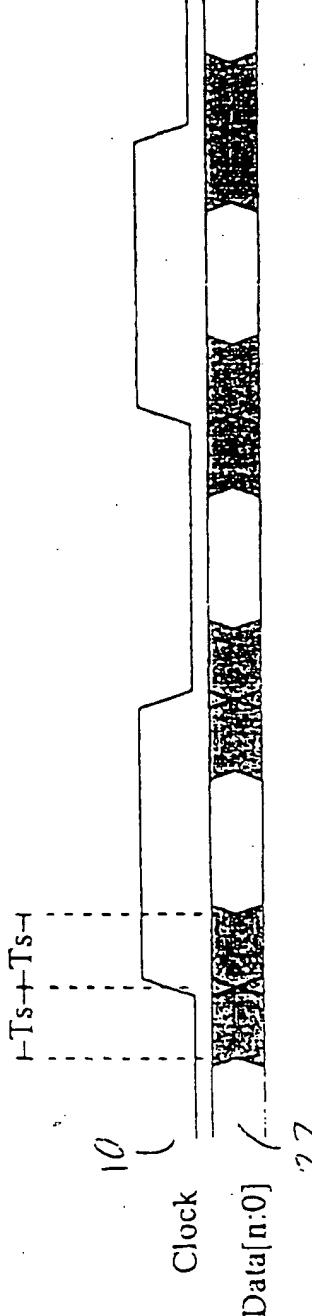


Fig. 2

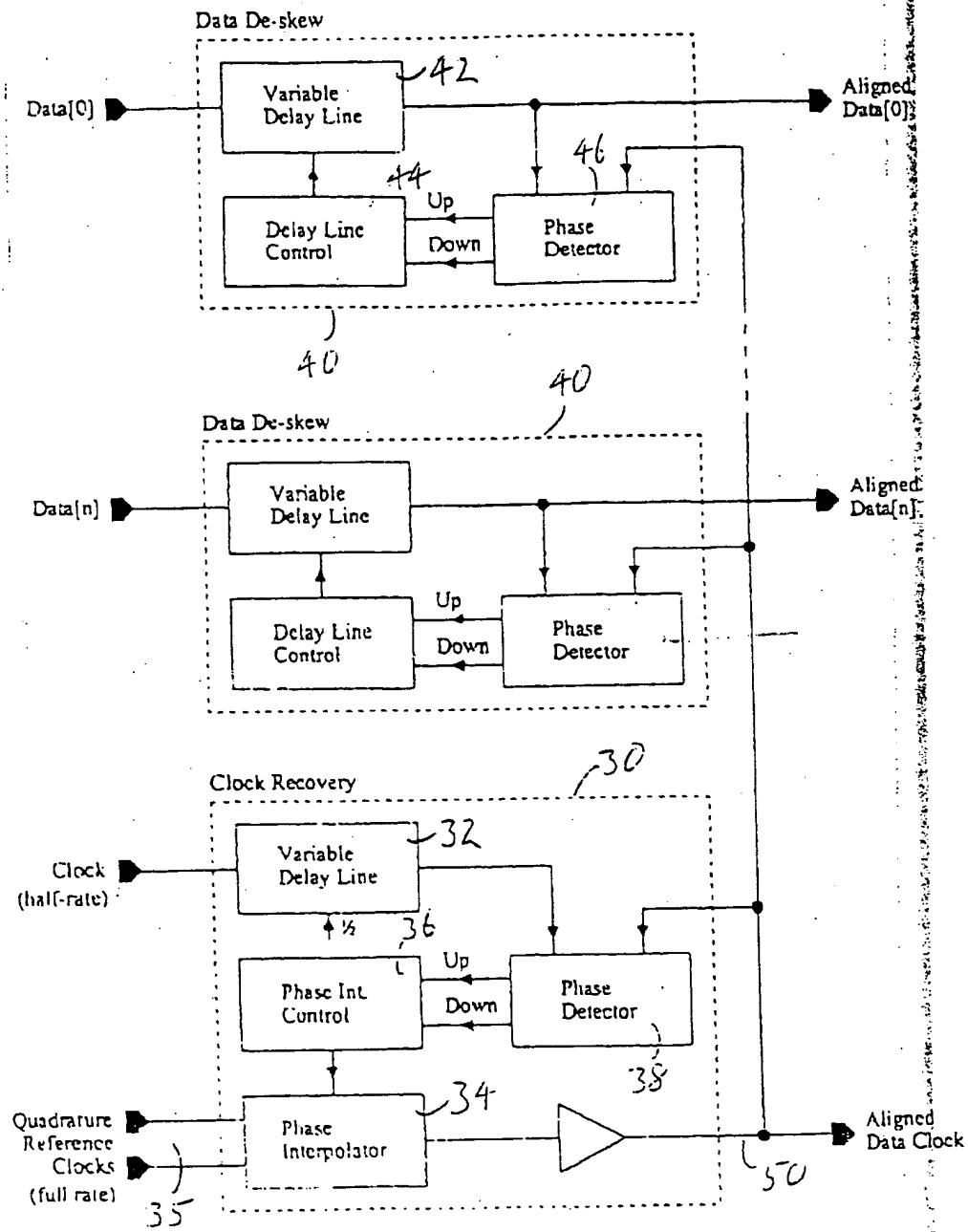


Fig. 3

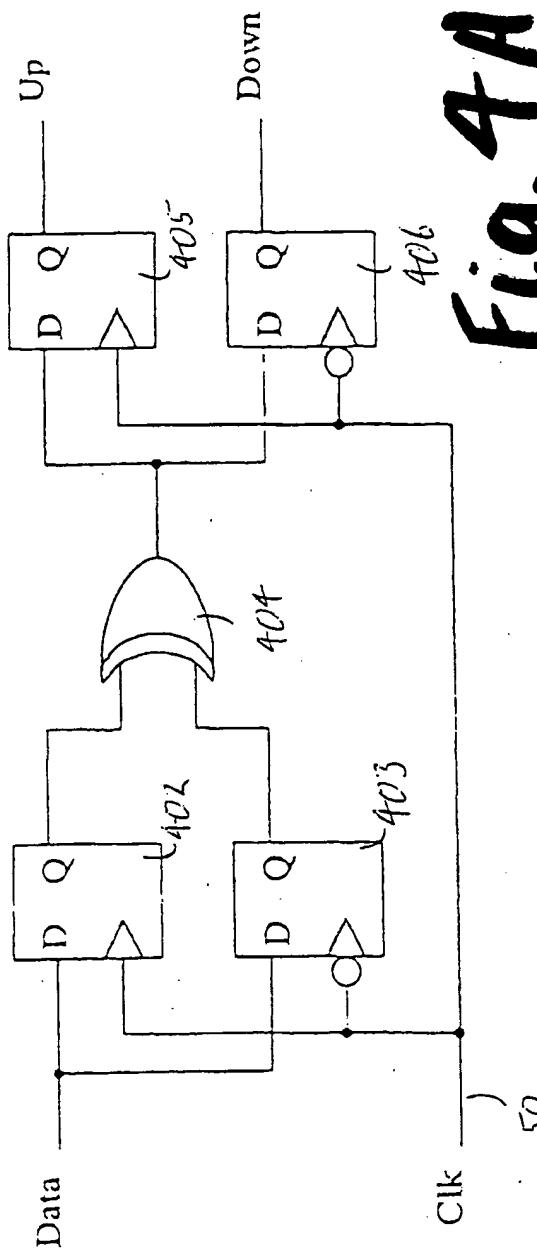


Fig. 4A

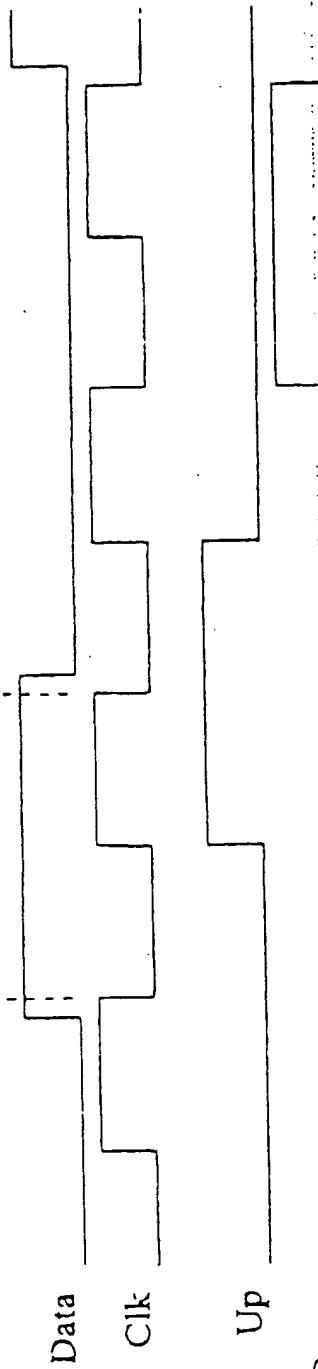


Fig. 4B

Down

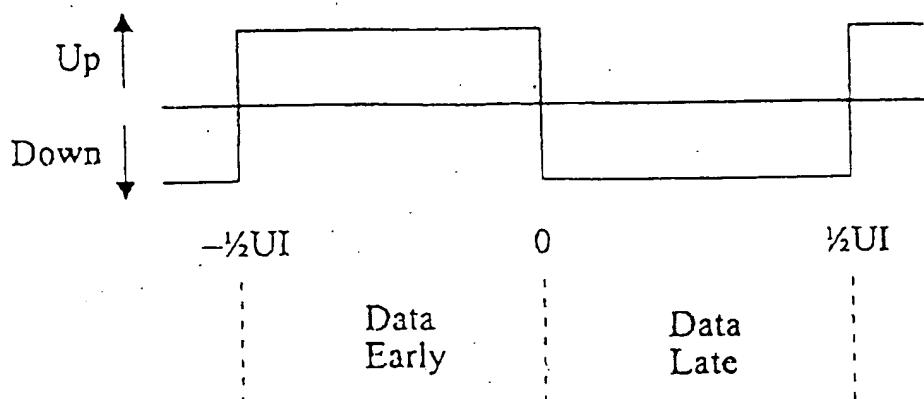


Fig. 5

Data delay adjustment range

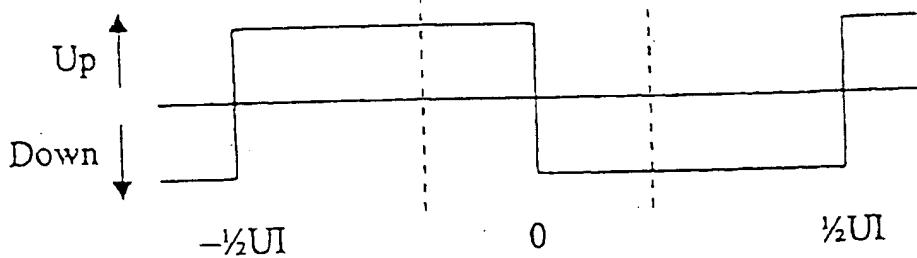
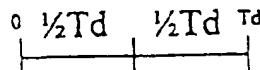
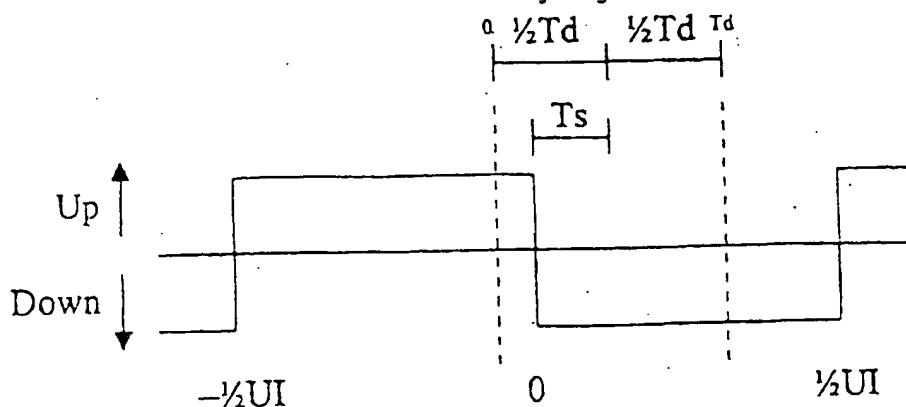
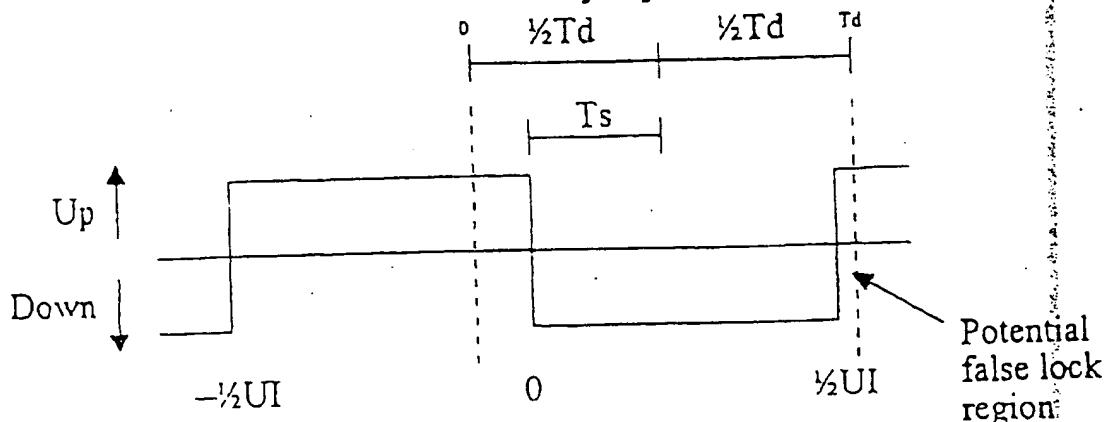


Fig. 6

Data delay adjustment range

Fig. 7

Data delay adjustment range

Fig. 8

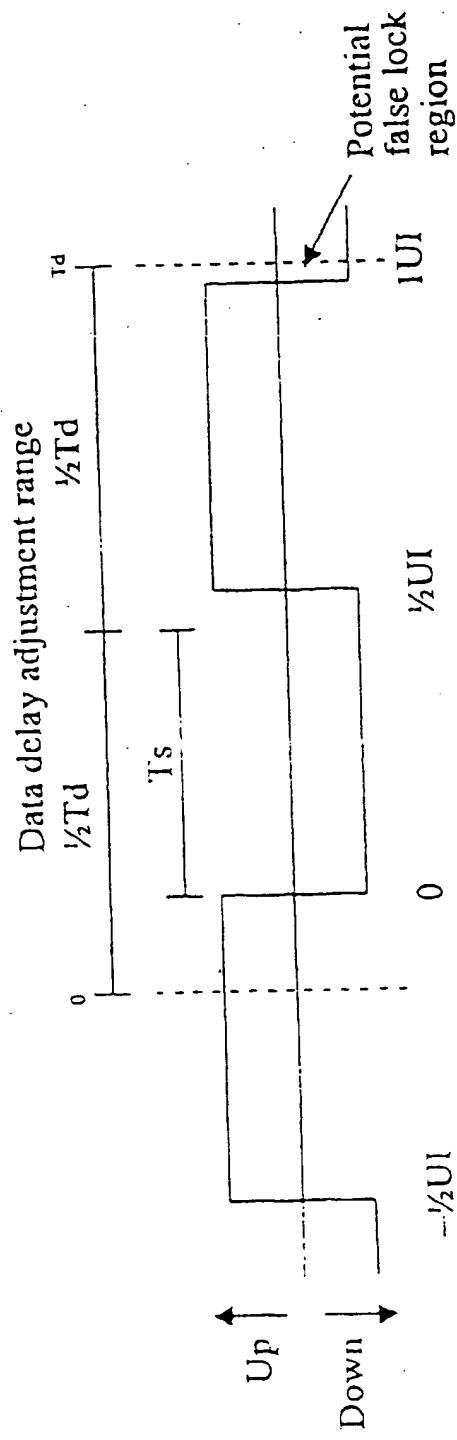


Fig. 9

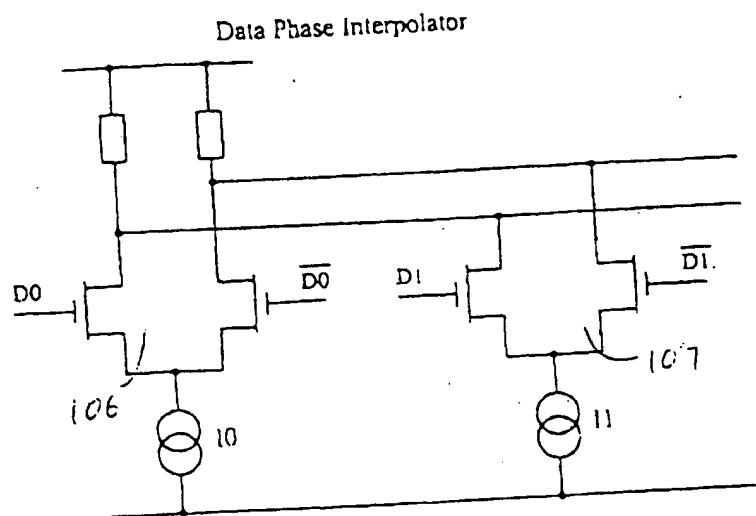
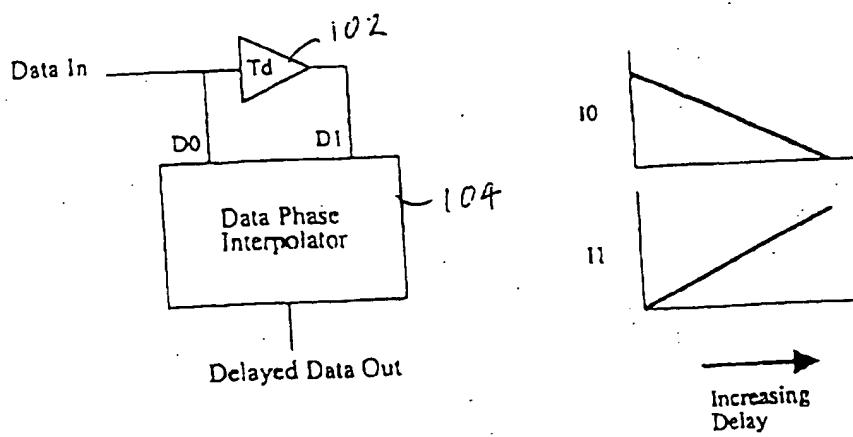


Fig. 10

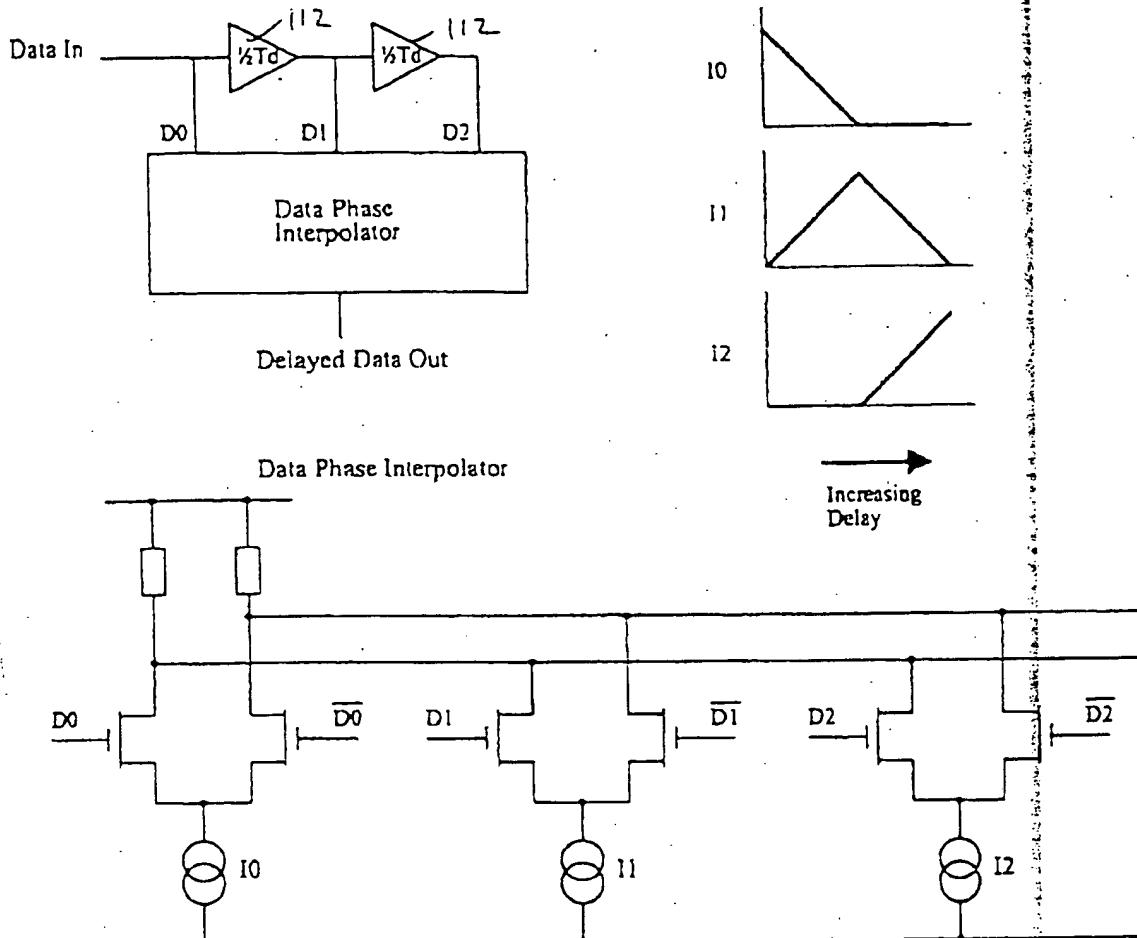


Fig. 11